CDF Operations Report

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CDF Weekly Meeting

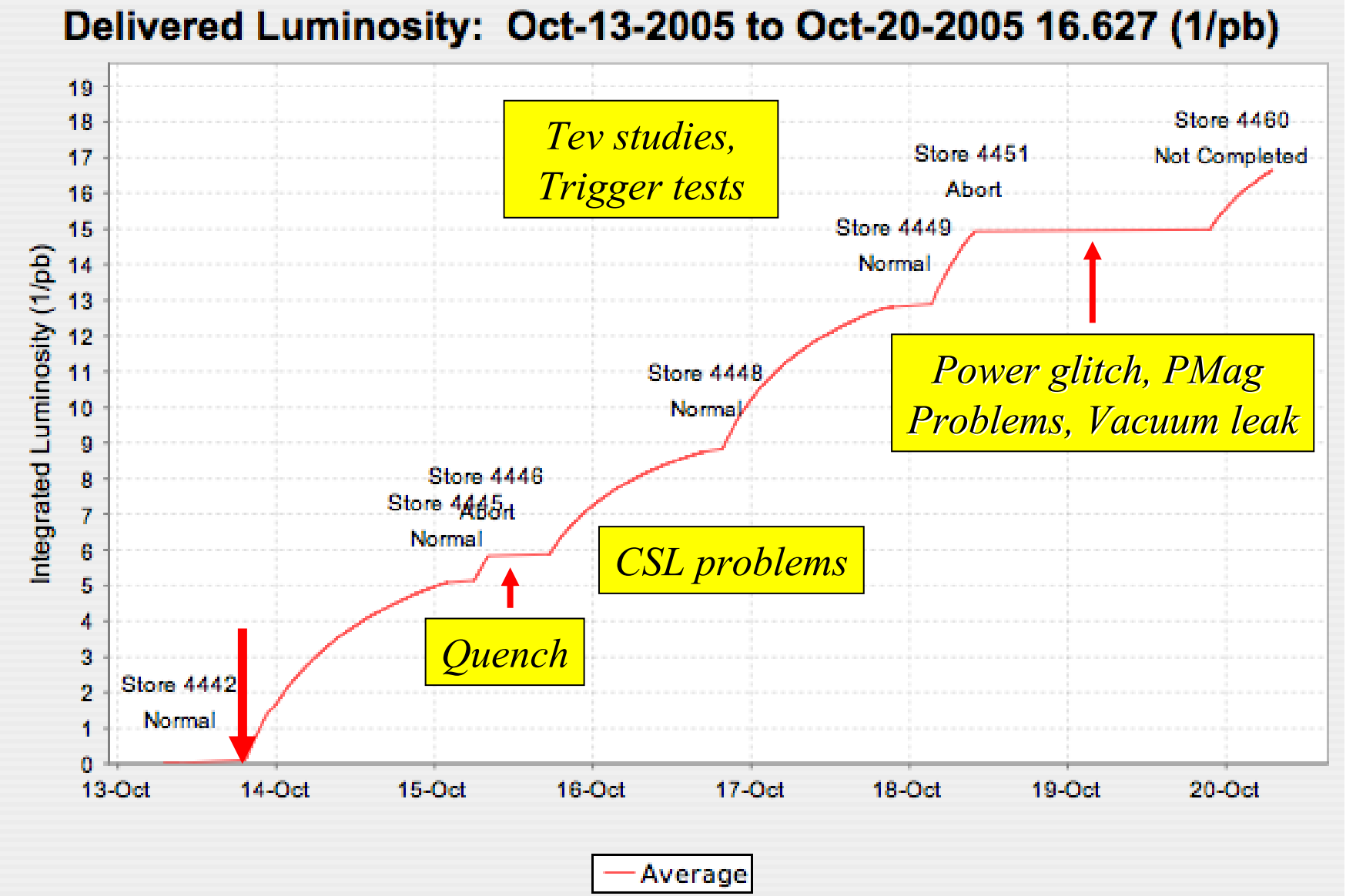
October 20, 2005

Outline:

- Record Initial Luminosity: 152.4E30
- Store summary
- CDF operation
- Access summary

Store Summary (13 – 20 Oct. 2005)

Store	start Date	time [hours]	initial lumi [E30]	int. lumi delivered [pb^{-1}]	live lumi [pb^{-1}]	GoodRun w/ si [pb^{-1}]	Comments
4445 Normal	10/13	31	**152.4**	5.5	3.6 64%	3.3 59%	**Record init. lumin.**
4446 abort	10/15	2.4	109	0.8	0.6 78%	0.6 78%	Vacuum burst at F0, quench
4448 Normal	10/15	23.7	90	3.0	2.4 80%	2.4 80%	Trigger table tests, CSL problems
4449 Normal	10/16	26.1	132.8	4.2	3.6 86.3%	3.5 85.4%	End-of-Store tests
4451 abort	10/18	6.9	142.9	2.2	1.6 72%	1.6 72%	Power glitch
4460	10/19	12.6	88.9	In progress			L3 reformatter errors
Total	10/13-10/20	102.7		15.7 pb^{-1}	11.8pb^{-1} 75%	11.4pb^{-1} 73%	


Delivered Luminosity: Oct-13-2005 to Oct-20-2005 16.627 (1/pb)
Integrated Luminosity (1/pb)
0
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
13-Oct
14-Oct
15-Oct
16-Oct
17-Oct
18-Oct
19-Oct
20-Oct
Store 4442
Normal
Store 4446
Store 4445
Abort
Normal
Store 4448
Normal
Store 4449
Normal
Store 4451
Abort
Store 4460
Not Completed
Tev studies,
Trigger tests
Quench
CSL problems
Power glitch, PMag
Problems, Vacuum leak
Average

System News

- Serious incident with the Silicon ladders (SB4W9L4, SB5W3L1)
- Important DAQ upgrade
  - New **SuperPanic** mode implemented, protects from high L1 rate (30 kHz). It could be extended to other (non Silicon) error conditions as well
- Online Computing
  - File server migration
  - ACNET problems at b0dap85
  - Upgrade to Linux 3.0.5, work in progress
  - Reconfiguring the quotas for RC logs on b0spool
  - Cluster-wide reboot done
- Several CSL problems
  - Lost connection with tape logger, then with one HD, b0dau32 rebooted
- Trigger table test
  - New default trigger tables implemented, ready for the 192 bit upgrade
- L3/EVB upgrade
  - Debugged missing XTCD bank (long term problem)
  - Debugged L3 reformatter

Access summary

Monday, October 17

- Silicon
  - Fiber swap for SB1W2L4, confirmed the problem with ladder
  - Probing the bias cable for IB2W2L3, confirmed the problem with ladder
  - Light out check from TX

Tuesday, October 18

- CEM
  - Bob Wagner fixed PMT problem, LED calibration done
- Silicon
  - Probing the power supply lines for IB4W0 and IB5W3
- Muon Detectors
  - CMP oscillating stack #60 fixed, but we got other stacks now oscillating #6 and #8
  - BMU oscillating channels removed (W 26, 27), reduced oscillation at SE stack #150.
- Forward Detectors
  - Tunnel cables prepared for the remote SMD crate power recycling

Access summary (cont'd)

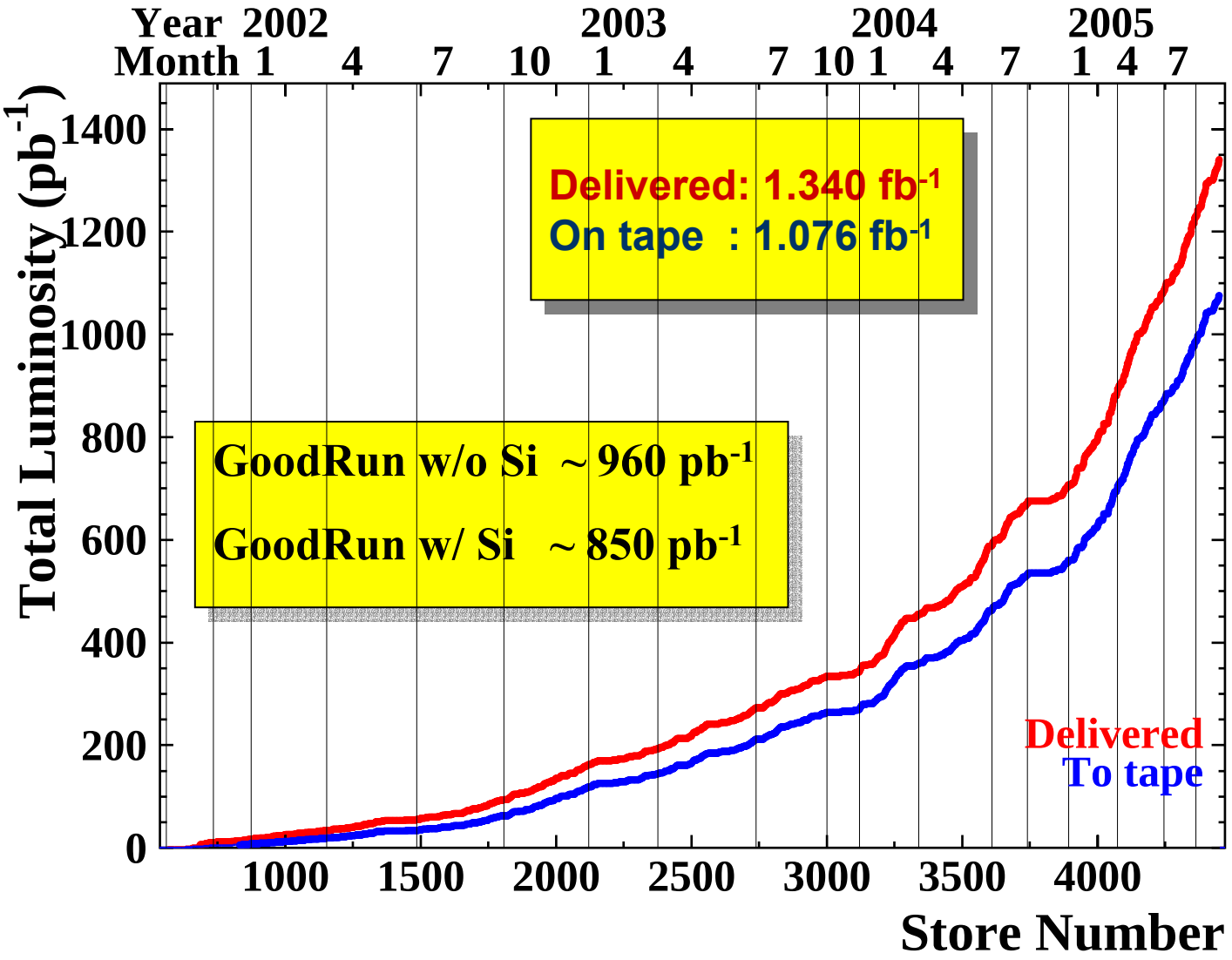
Tuesday, October 18

- XTC2 & TDC work
  - Replaced XTC2 boards
  - Installed several transition boards
  - Replaced TDC in COT15 crate

- Radiation monitoring
  - Rick and Andy measured I/V curves

- CLC work
  - Failing HV boards, now we are able to reproduce the problem

Run II Integrated Luminosity



Thank you, Bob!